



PATENT NUMBER

U.S. UTILITY Patent Application

KC	O.I.P.E.	PATENT DATE
SCANNED <u>LH</u>	O.A. <u>mc</u>	

APPLICATION NO. 09/531026	CONT/PRIOR D	CLASS 703	SUBCLASS 14	ART UNIT 2763	EXAMINER <i>Baada</i> DAY
------------------------------	-----------------	--------------	----------------	------------------	------------------------------

Sudheendra Hanga
James O'Connor

System and method for validating processor performance and functionality

PTO-2040
12/99

ISSUING CLASSIFICATION

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	

WARNING:

The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

**Form PTO-438A
(Rev. 8/80)**

FILED WITH: DISK (CRF) FICHE CD-ROM

(Attached to postcard on date inside flap)

AVAILABLE COPY